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FORM PTO-1449 (Modified)	Attorney Docket No.: 016747-010010US	Application No.: 09/802,289
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	Applicant: Ashley Saulsbury, et al.	
	Filing Date: March 8, 2001	Group: 2131

Reference Designation **U.S. PATENT DOCUMENTS** Page 1

Examiner Initial	Document No.	Date	Name	Class	Sub-class	Filing Date (If Appropriate)
AA	4,725,945	02/16/88	Kronstadt et al.	364	200	
AB	4,894,770	01/16/90	Ward et al.	364	200	
AC	5,184,320	02/02/93	Dye	365	49	
AD	5,261,066	11/09/93	Jouppi et al.	395	425	
AE	5,317,718	05/31/94	Jouppi	395	425	
AF	5,386,547	01/31/95	Jouppi	395	425	
AG	5,564,035	10/08/96	Lai	395	471	
AH	5,588,130	12/24/96	Fujishima et al.	395	445	
AI	5,623,627	04/22/97	Witt	395	449	
AJ	5,649,154	07/15/97	Kumar et al.	395	449	
AK	5,650,955	07/22/97	Puar et al.	365	51	
AL	5,687,338	11/11/97	Boggs et al.	395	381	
AM	5,703,806	12/30/97	Puar et al.	365	181	
AN	5,900,011	05/04/99	Saulsbury et al.	711	119	
AO	6,128,702	10/03/00	Saulsbury et al.	711	133	

FOREIGN PATENT DOCUMENTS

	Document No.	Date	Country	Class	Sub-class	Translation (Yes/No)
AP						
AQ						
AR						
AS						
AT						
AU						

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

AV	
AW	
AX	
AY	
AZ	

EXAMINER <i>Henry Jan</i>	DATE CONSIDERED <i>12/31/03</i>
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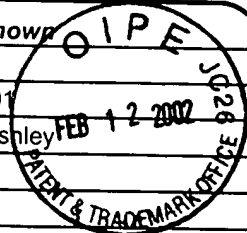
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Substitute for form 1449B/PTO		Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Application Number	09/802,289
		Filing Date	March 8, 2001
		First Named Inventor	Saulsbury, Ashley
		Group Art Unit	2131
		Examiner Name	
Sheet 3 of	Attorney Docket Number	016747-010010US	



U.S. PATENT DOCUMENTS						
Examiner Initials *	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
24	A	4,980,819		Cushing et al.	12/25/1990	
25	B	5,530,817		Masubuchi	06/25/1996	
26	C	5,301,340		Cook	04/05/1994	

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Examiner Initials *	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Office ³	Number ⁴	Kind Code ⁵ (if known)				
24	D	PCT	WO 00/33178		Sun Microsystems, Inc.	06/08/2000		

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
24			

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Examiner Signature	<i>[Signature]</i>	Date Considered	12/31/03
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)			Complete if Known		
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U.S. PATENT DOCUMENTS+					
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	AA	US-5,710,907	1/1998	Hagersten et al.	RECEIVED NOV 13 2003 Technology Center 2600
	AB	US-5,953,738	9/1999	Rao	
	AC	US-6,000,007	12/1999	Leung et al.	
	AD	US-6,202,143	3/2001	Rim	
	AE	US-6,128,700	10/2000	Hsu et al.	
	AF	US-6,256,256	7/2001	Rao	
	AG	US-6,275,900	8/2001	Liberty	
	AH	US-6,321,318	11/2001	Baltz et al.	
	AI				

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		Country Code ³	Number ⁴	Kind Code ⁵ (if known)				
	AJ							<input type="checkbox"/>
	AK							<input type="checkbox"/>

NON PATENT LITERATURE DOCUMENTS			
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	AL		
	AM		

Examiner Signature	<i>Henry Lee</i>	Date Considered	12/31/03
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Sheet 3 of 3

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	AE							
	AF							
	AG							
	AH							

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	AI	AIMOTO, YOSHIHARU et al.; "A.768GIPS 3.84GB/s 1 W Parallel Image-Processing RAM Integrating a 16 Mb DRAM and 128 Processors"; ISSCC96/Session 23 / DRAM / Paper SP23.3; 1996 IEEE International Solid-State Circuits Conference; pp. 372-373 and 476	
	AJ	BURSKY, DAVE; "Combo RISC CPU and DRAM Solves Data Bandwidth Issues"; Electronic Design; March 4, 1996; pp. 67-71.	
	AK	SAULSBURY, ASHLEY, et al., "Missing the Memory Wall: The Case for Processor/Memory Integration"; ACM; 1996; pp. 90-101	
	AL	SHIMIZU, TORO, et al.; "A Multimedia 32b RISC Microprocessor with 16 Mb DRAM"; ISSCC96/Session 13 / Microprocessors/Paper FP 13.4; 1996 IEEE International Solid State Circuits Conference; pp. 216-217 and 448	
	AM	MITSUBISHI ELECTRIC CORP; Product Specification for Single-Chip 32-Bit CMOS Microcomputer; © May 1998	

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